

ABSTRACT OF THE DISCLOSURE

A driving circuit of a liquid crystal display device having driver output lines connected to outputs of a data line driver, m pieces of block selection signal lines for sequentially selecting m pieces of blocks, data lines for supplying data to a display area, and a switch for sequentially connecting an ith driver output line to ith, $i+2j$ th, ..., and $i+2j \times (m-1)$ th data lines in response to signals on the m pieces of block selection signal lines when j is a positive integer smaller than m.

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